

**SPECIFICATION
OF
LCD MODULE**

MODULE NO.: HL052T04-01

Customer Approval:

☐ **Accept**

☐ **Reject**

	SIGNATURE	DATE
PREPARED BY		
CHECKED BY		
APPROVED BY		

DOCUMENT REVISION HISTORY

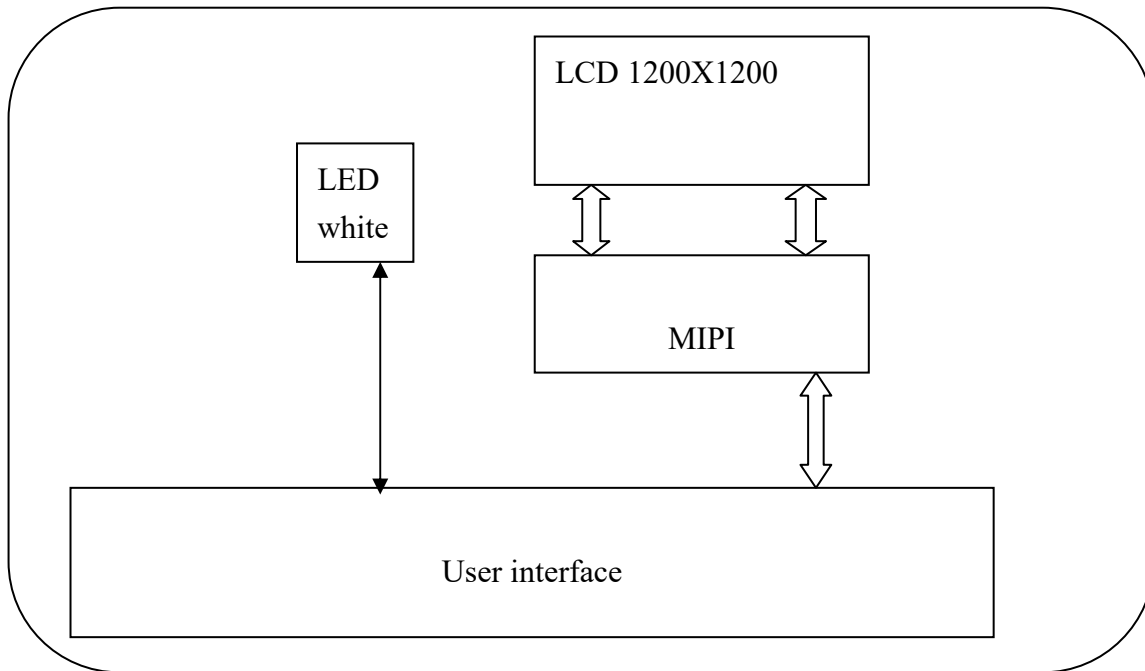
Sample Version	Doc. Version	DATE	DESCRIPTION	CHECKED BY
0001	A0	2026-3-20	First Release.	

1. MECHANICAL SPECIFICATIONS:

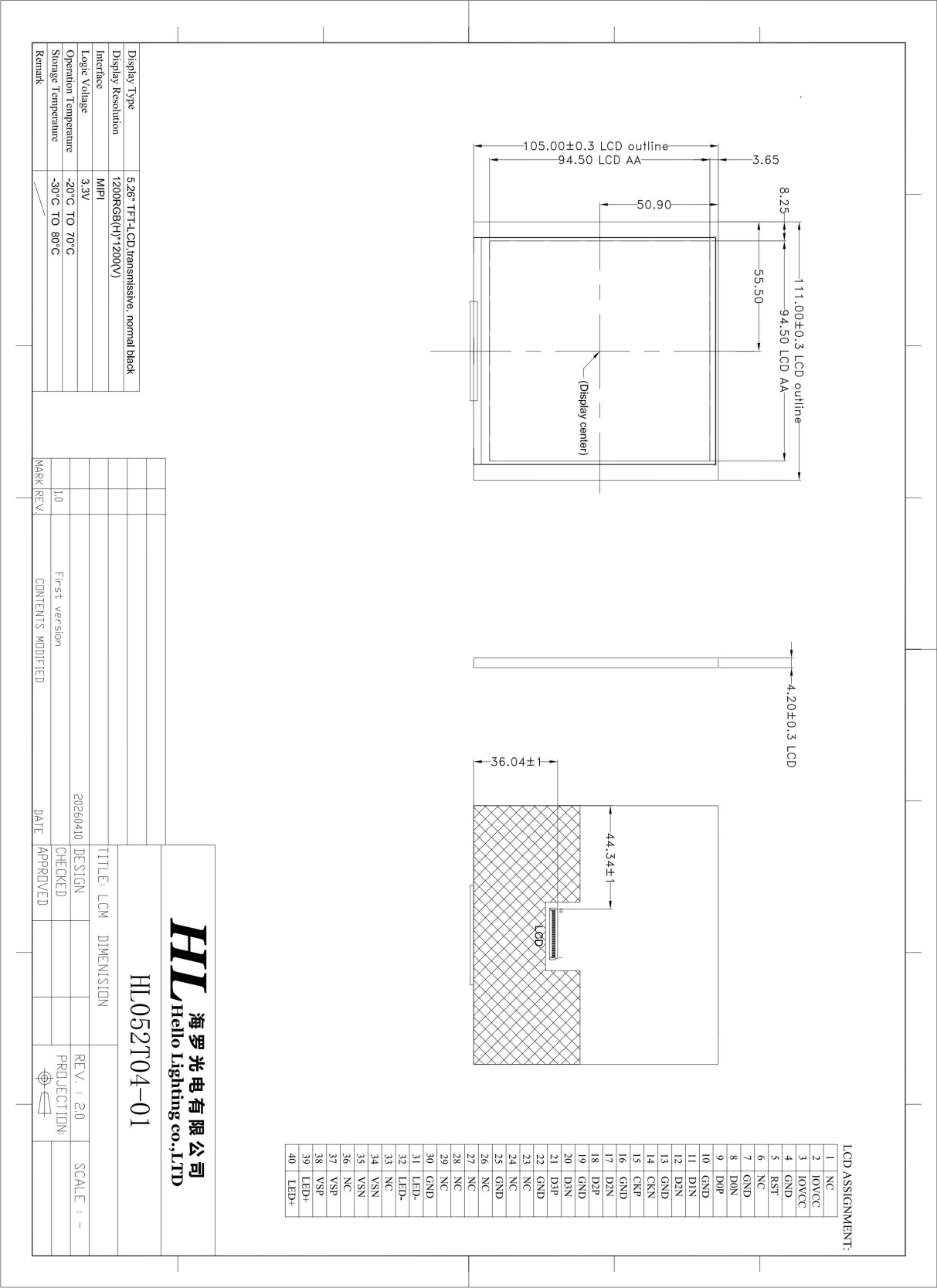
ITEM	SPECIFICATION	UNIT
OUTLINE DIMENSIONS	110(W)*105(H)*4.2(D)	mm
ACTIVE AREA	94.5(W)*94.5(H)	mm
DISPLAY SIZE	5.2	inch
DOT PITCH	0.07875mm*0.07875mm	mm
RESOLUTION	1200 (RGB) ×1200	-
LCD TYPE	Normal white	-
BACKLIGHT TYPE	LED White	-

***See attached drawing for details.**

2.BLOCK DIAGRAM:



3.DIMENSIONAL



4. PIN DESCRIPTION:

4.1 pin define for LCD

connector on LCD: HRS: FH33J-40S-0.5SH(10)

NO.	PIN NAME	I/O	Description
1	NC	P	No connection
2	IOVCC	P	Power supply for system, IOVCC=1.8v
3	IOVCC	P	
4	GND	P	Ground
5	RST	I	Device reset signal
6	NC	-	No connection
7	GND	P	Ground
8	MIPI_0N	I	MIPI Negative data inputs
9	MIPI_0P	I	MIPI Positive data inputs
10	GND	P	Power ground
11	MIPI_1N	I	MIPI Negative data inputs
12	MIPI_1P	I	MIPI Positive data inputs
13	GND	P	Power ground
14	MIPI_CKN	I	MIPI Negative clock inputs
15	MIPI_CKP	I	MIPI Positive clock inputs
16	GND	P	Power ground
17	MIPI_2N	I	MIPI Negative data inputs
18	MIPI_2P	I	MIPI Positive data inputs
19	GND	P	Power ground
20	MIPI_3N	I	MIPI Negative data inputs
21	MIPI_3P	I	MIPI Positive data inputs
22	GND	P	Power ground
23	NC	-	No connection
24	NC	-	No connection
25	GND	P	Power ground
26	TE	O	Tearing effect output
27	NC	-	No connection
28	NC	-	No connection
29	NC	-	No connection
30	GND	P	Power ground
31	LED-	-	No connection
32	LED-	-	No connection
33	NC	-	No connection

34	VSN	P	Analog supply negative voltage(-5~-6V)
35	VSN	P	Analog supply negative voltage(-5~-6V)
36	NC	-	No connection
37	VSP	P	Analog supply positive voltage(5~6V)
38	VSP	P	
39	LED+	-	No connection
40	LED+	-	No connection

Note:

1: P : power supply,

I: host to LCD

O: LCD to host

IO: LCD to host or host to LCD

5. MAXIMUM ABSOLUTE LIMIT:

Item	Symbol	Value	Unit	Remarks
Power supply voltage for logic	IOVCC	-0.3~4.0	V	Note 1
Analog supply positive voltage	VSP	-0.3~6.5	V	
Analog supply negative voltage	VSN	-6.5~0.3	V	
Power supply for heater	HEAT+	-0.3~15	V	
Input Signal Voltage	V _{iH}	0.7VCC~VCC	V	
Input Signal Voltage	V _{iL}	0 ~0.3VCC		
Operating temperature	Topr	-20 to 70	°C	Note 2
Storage temperature	Tstg	-30 to 80	°C	

Note: Note (1) Permanent damage to the device may occur if maximum values are exceeded
functional operation should be restricted to the condition described under normal
operating conditions.

Note (2) Temperature and relative humidity range are shown in the figure below.

95 % RH Max. (40°C $\geq$ Ta)

Maximum wet -bulb temperature at 39°C or less. (Ta > 40°C) No condensation.

6.ELECTRICAL CHARACTERISTICS

6-1 DC Characteristics (Ta=25°C)

Item	Symbol	Min	Type	Max	Unit	Test condition
Operating voltage	IOVCC	1.7	1.8	3.6	V	-
Voltage for analog negative	VSN	-5.7	-5.5	-5.3	V	
Voltage for analog positive	VSP	5.3	5.5	5.7	V	
Supply current	I _{DD} (IOVCC)	-	12	24	mA	Ta=25°C
Supply current	I _{DD} (VSN)	-	10	13	mA	Ta=25°C
Supply current	I _{DD} (VSP)	-	10	13	mA	Ta=25°C
Input voltage	V _{IH}	0.7IOVCC	-	IOVCC	V	-
	V _{IL}	0	-	0.3IOVCC	V	
Input leakage current	I _{IL}	-1.0	-	1.0	μA	V _{IN} =IOVCC or V _{SS}

6-2 Backlight Electrical-optical Characteristics

1. Stander Lamp Styles(Edge Lighting Type):

The LED chips are distributed over the edge light area of the illumination unit, which gives the less power consumption:

2. The Main Advantages of the LED Backlight are as following:

2.1 The brightness of the backlight can simply be adjusted by a resistor or a potentiometer.

3. Data About LED Backlight:

Item	Symbol	MIN	TYP	MAX	UNIT	Test Condition	Note
Supply Voltage for white	V _{white}	-	13	14.3	V		-
Supply Current for white	I _{white}	-	135	150	mA	-	-
Uniformity for LCM	-	70	-	-	%		3
Life Time	-	30000	-	-	Hr		-
Backlight Color	White						

NOTE:

1. Average Luminous Intensity of P1-P9

2. Uniformity = Min/Max * 100%

3.LED life time defined as follows: The final brightness is at 70% of original brightness

Measured Method: (X*Y: Light Area)(Left Draft as follow)

Internal Circuit Diagram(Right Draft as follow)

(Effective spatial Distribution)

Hole Diameter ø 3mm ; 1 to 9 per Position Measured Luminous:

7.DISPLAY TIMING(VIDEO MODE)

7-1 MIPI interface timing

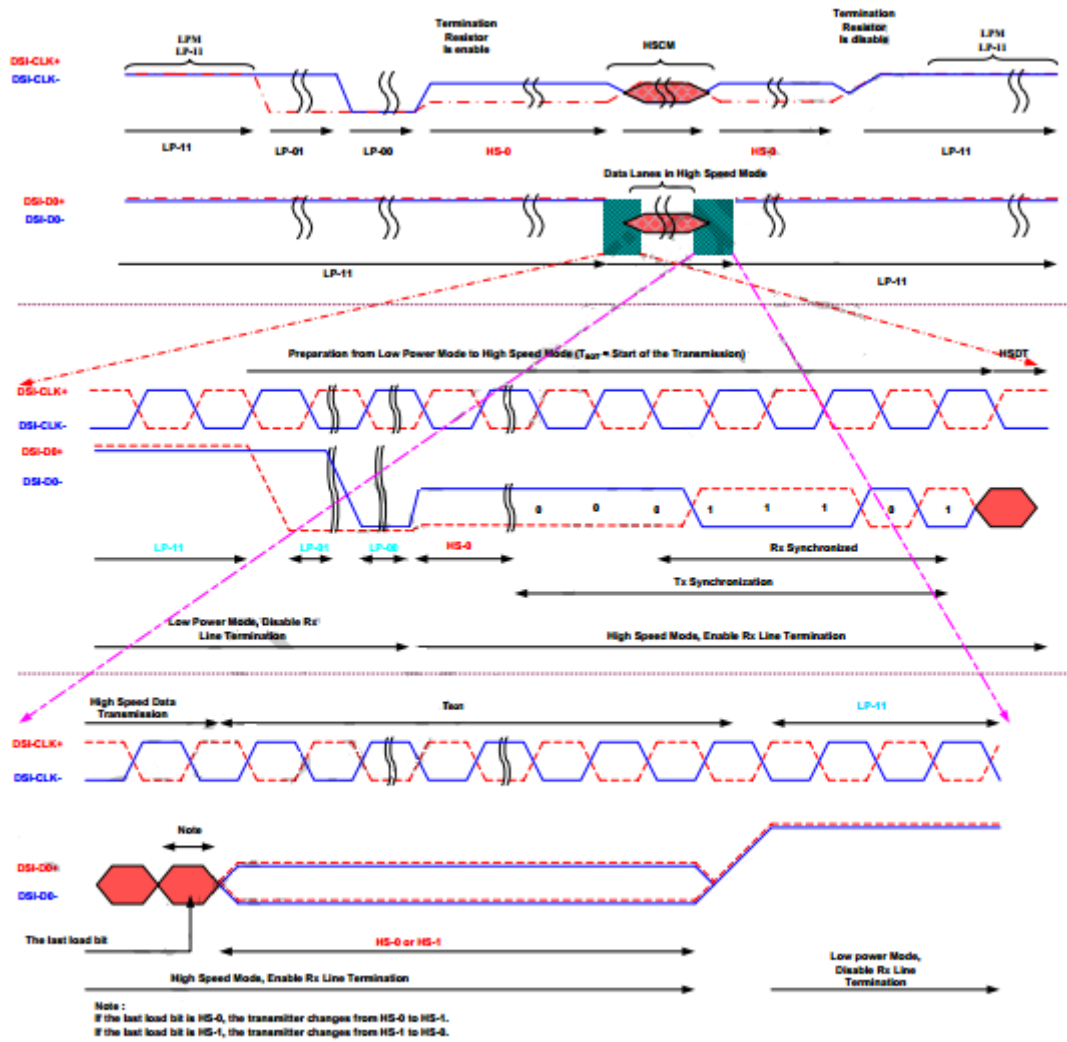
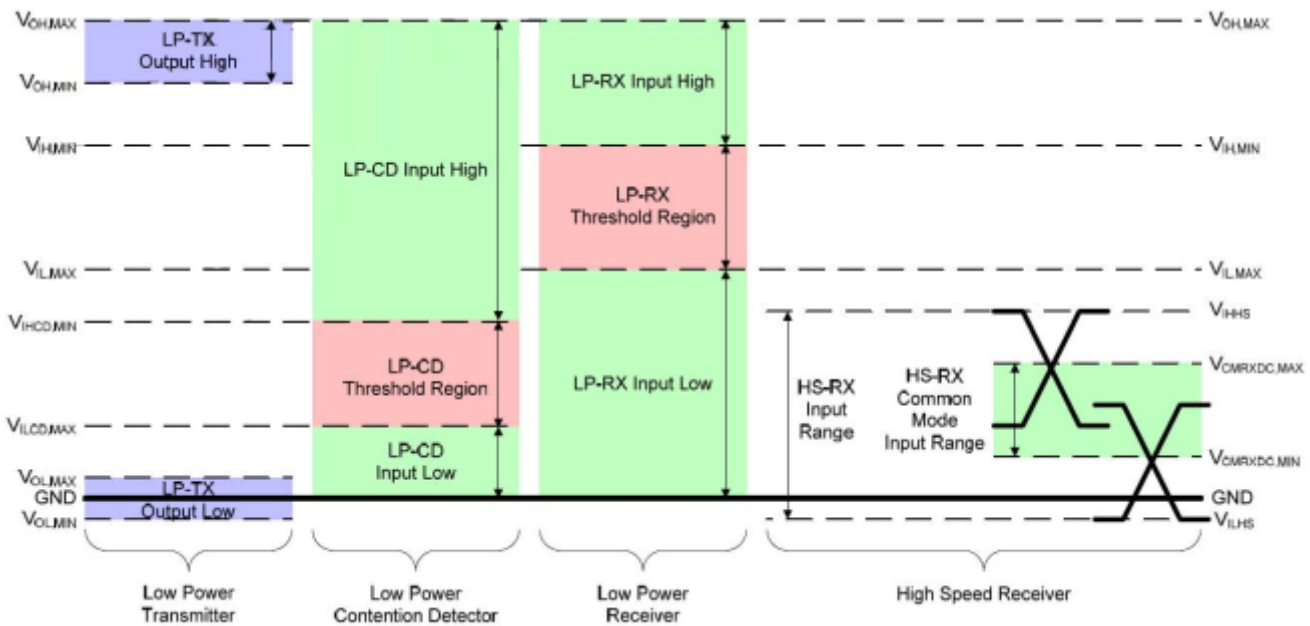


Figure: High speed clock burst

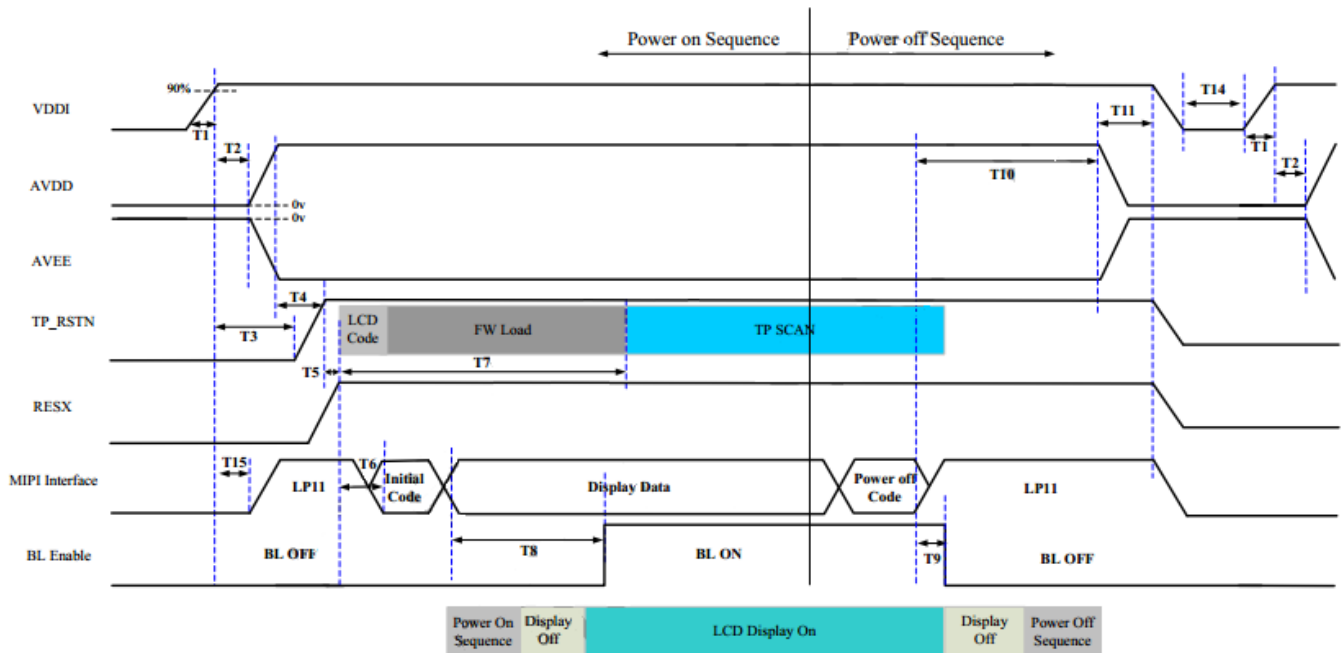
(AVDD = 5.0V~6.5V, AVEE = -5.0V~-6.5V, VDDI = 1.65V~3.6V, Ta = -30°C ~ 70°C)

Parameter	Symbol	Conditions	Specification			Unit
			MIN	TYP	MAX	
Power supply voltage for MIPI Interface						
Power supply voltage for MIPI interface	VDDAM	-	1.65	1.8	3.6	V
	LVDSVDD	-	1.125	1.3	1.5	V
LPDT Input Characteristics						
Pad signal voltage range	VI	-	-50	-	1350	mV
Ground Shift	VGNDSH	-	-50	-	50	mV
Logic 0 input threshold	VIL	-	0	-	550	mV
Logic 1 input threshold	VIH	-	880	-	LVDSVDD	mV
Input hysteresis	VHYST	-	25	-	-	mV
LPDT Output Characteristics						
Output low level	VOL	-	-50	-	50	mV
Output high level	VOH	-	1.1	1.2	1.3	V
Logic 1 contention threshold	VIHCD,MIN	-	450	-	LVDSVDD	mV
Logic 0 contention threshold	VILCD,MAX	-	0	-	200	mV
Output impedance of LPDT	ZOLP	-	80	100	125	ohm
Hi-speed Input/Output Characteristics						
Single-end input low voltage	VILHS	-	-40	-	-	mV
Single-end input high voltage	VIHHS	-	-	-	460	mV
Single-end threshold for HS termination enable	VTERM-EN	-	-	-	450	mV
Differential input low threshold	VIDTL	-	-70	-	-	mV
Differential input high threshold	VIDTH	-	-	-	70	mV
Common mode voltage	VCMRXDC	-	70	-	330	mV
Differential input impedance	ZID	-	80	100	125	ohm



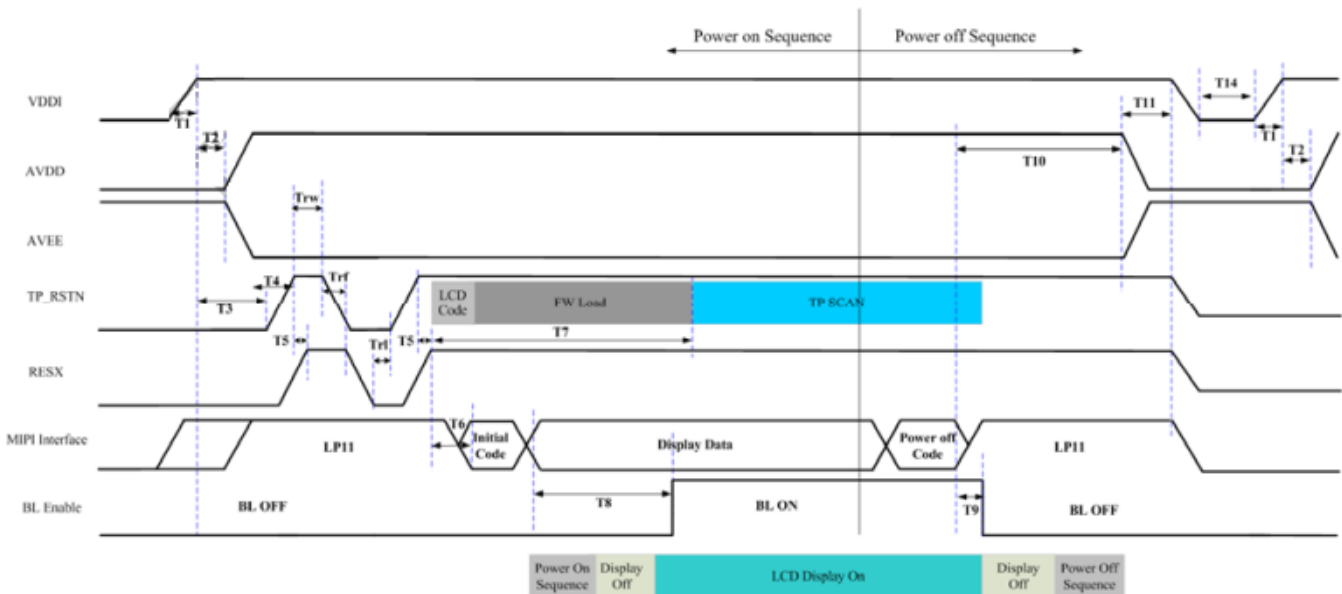
7-2 Power sequence

Power On/Off Sequence

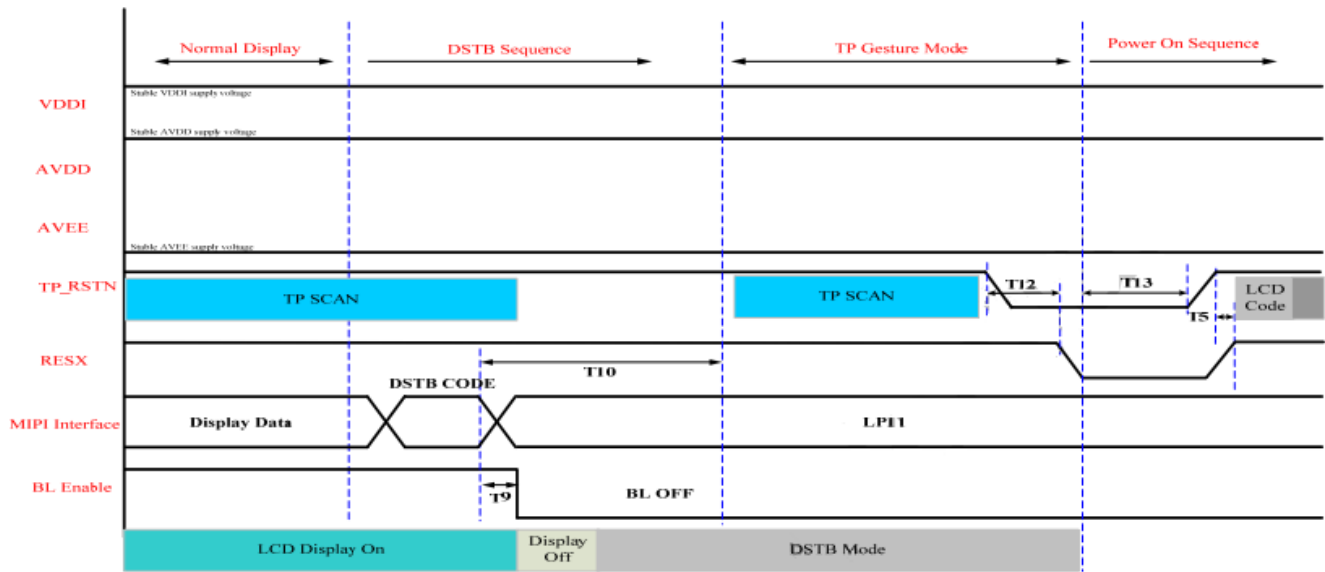


If system cannot ensure the MIPI interface power up after VDDI, 2nd reset is suggested to be used as below diagram.

Power On/Off Sequence with 2nd Reset



TP Gesture Mode Exit Sequence



Power on/off sequence timing

Parameter	Description	min	max	unit
T1	Rise time 0.1VDDI to 0.9VDDI	0	5	ms
T2	AVDD/AVEE rise after VDDI power on	1		ms
T3	TP reset time after VDDI power on	5		ms
T4	TP reset release time after AVDD power on	100		us
T5	TP reset release to LCD reset release	0		ms
T6	FLASH download LCD code after LCD reset	120		ms
T7	LCD reset release to TP scan start	150		ms
T8	LED on after initial code	150		ms
T9	LED off after power off code	50		ms
T10	AVDD/AVEE power down after power off code	150		ms
T11	VDDI power down after AVDD/AVEE power down	5		ms
T12	TP reset time before LCD reset	5		ms
T13	RESX reset falling to TP reset release	5	120	ms
T14	VDDI rise again after previous VDDI powered down	50		ms
T15	MIPI signals start(Hi-Z/GND to LP11) after VDDI power on	3		ms

8. OPTICAL CHARACTERISTICS:

Driving the backlight

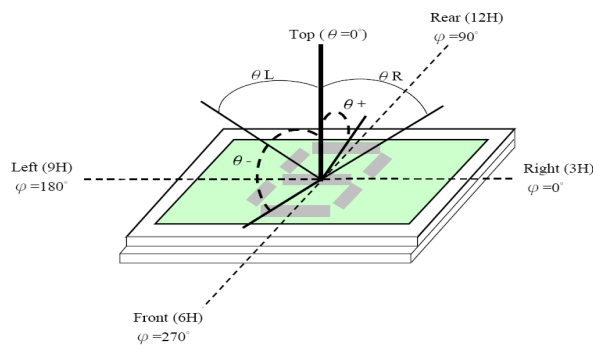
No.	ITEM		Symbol	Conditions	Specification			Unit	Note
					Min	Typ	Max		
1	Response Time		Tr+Tf	25℃	-	25	35	Ms	(1)(2)
2	Contrast Rate		Cr	θ=0, Normal viewing angle	1200	-	-	-	(1)(3)
3	Viewing Angle	Hor.	θL	CR>10	70	80	-	Deg	3
			θR		70	80	-		
		Ver.	Θ+		70	80	-		
			Θ-		70	80	-		
4	Chromaticiry	White	x	Brightness is ON		0.281	0.311		4
			y			0.310	0.340		
5	Luminance White color		L		750	1000		cd/m2	
6	Uniformity		U		75			%	
7	Luminance Y-G color		L			TBD		cd/m2	

Measure Conditions:

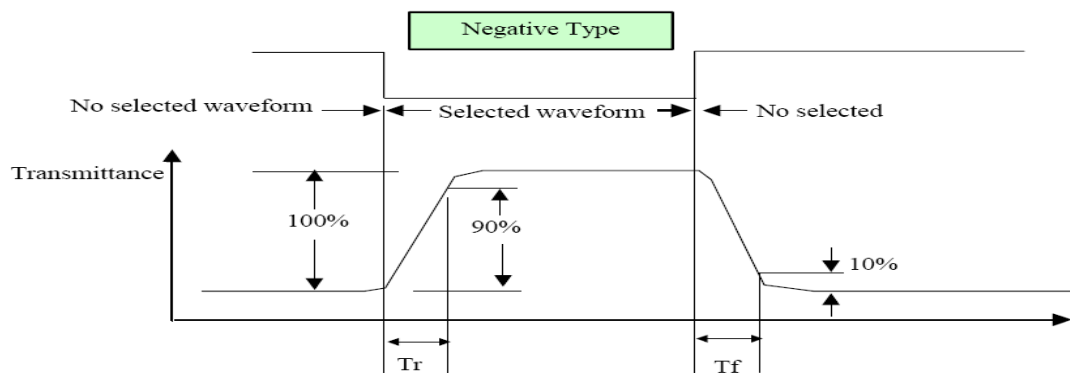
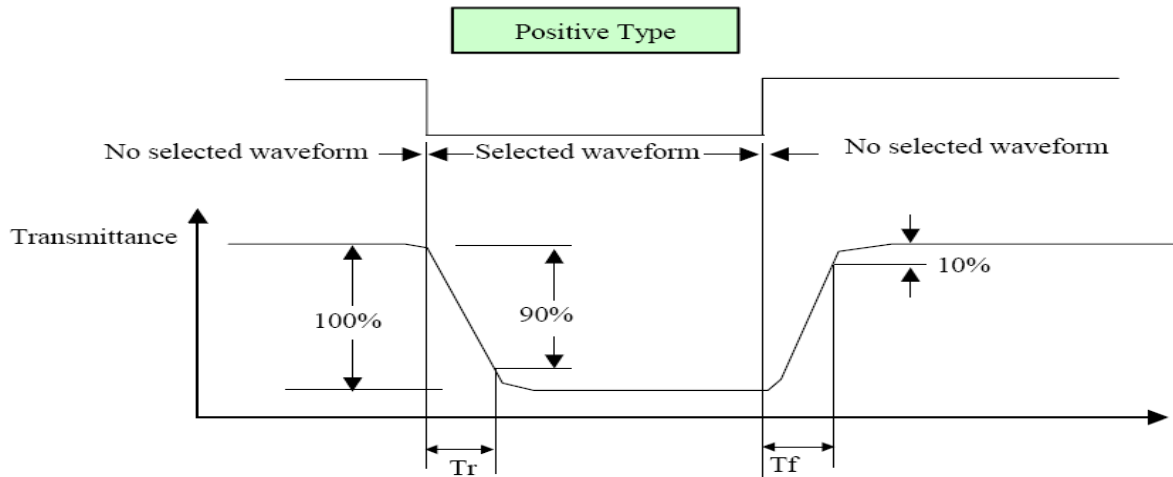
1. Measure surrounding : dark room;
2. Ambient temperature: $25\pm 2^{\circ}\text{C}$;
3. 30min.warm-up time.

Note Definition:

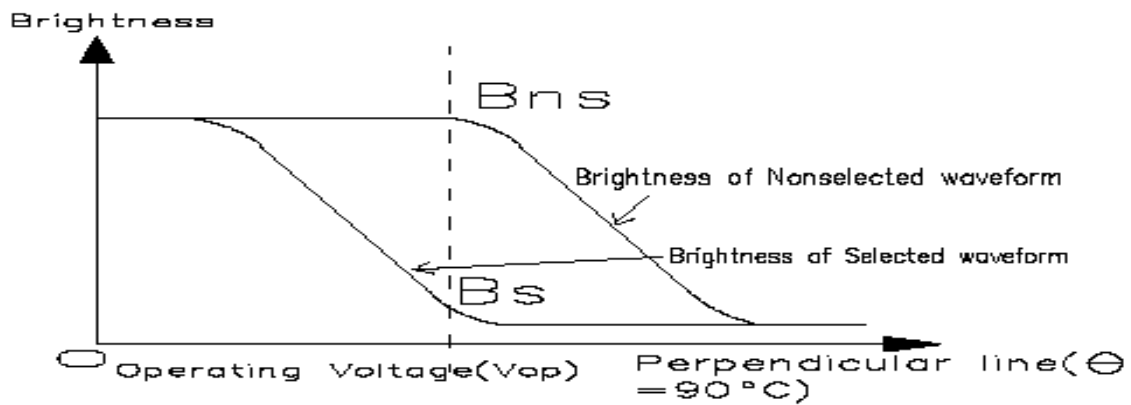
Note(1)Viewing angle range:



Note (2)Response Time:

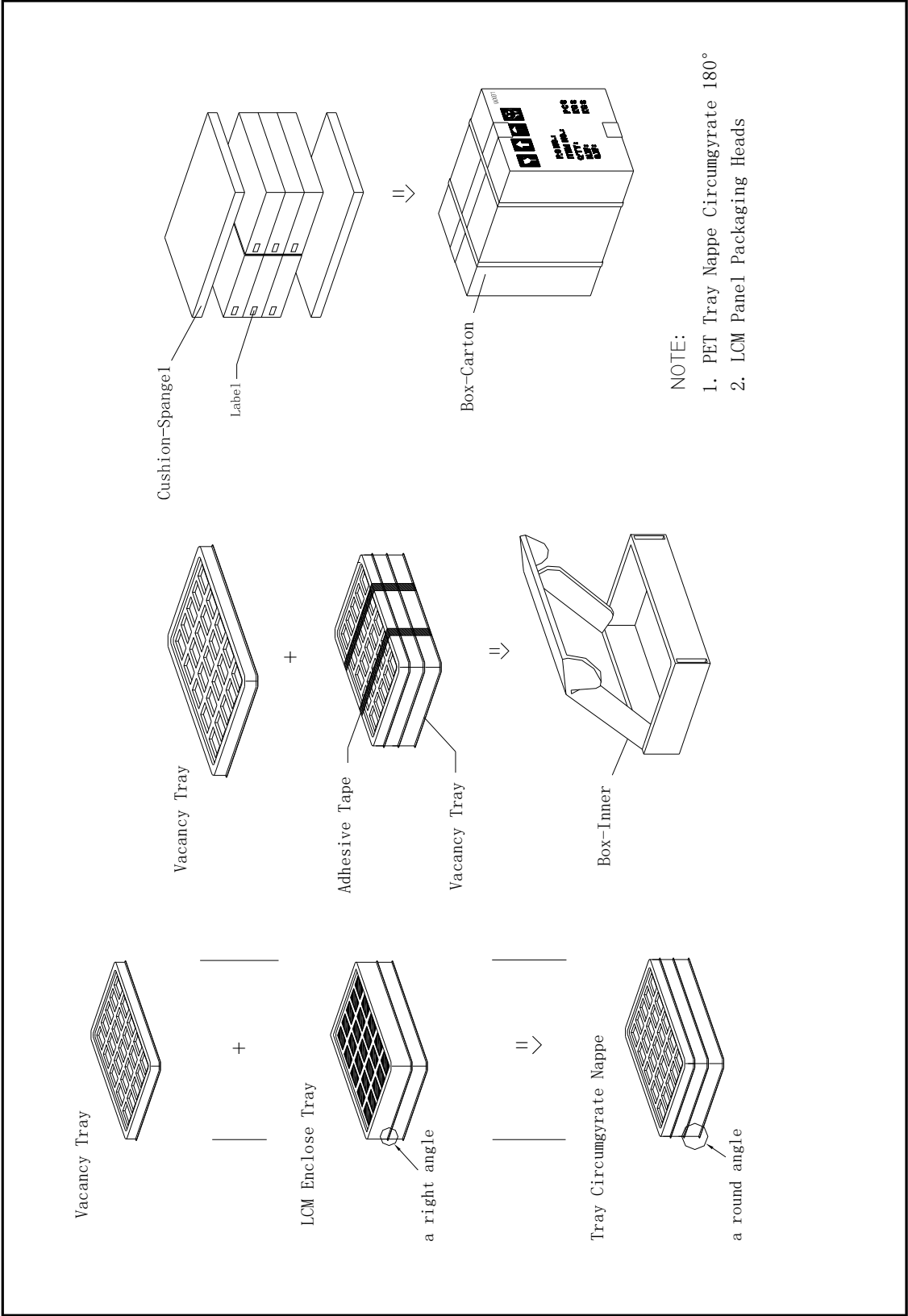


Note(3) Contrast Ratio Definition:



$$\text{Contrast Ratio (Cr)} = \frac{\text{Luminance with all pixel white}}{\text{Luminance with all pixel black}}$$

9.PACKAGE.



10. STANDARD SPECIFICATION FOR RELIABILITY:

Item	Condition		Time (hrs)	Assessment
High temp. Storage	80°C		120	No abnormalities in functions and appearance
High temp. Operating	70°C		120	
Low temp. Storage	-30°C		120	
Low temp. Operating	-20°C		120	
Humidity	40°C/ 90%RH		120	
Thermal Shock Temp. Cycle	-20°C ← →70°C (0.5hour ← → 0.5 hour)		10cycles	
ESD Testing	HBM:	±8KV		330Ω/150PF
	MM:	±200V		200PF/0Ω

Functions, performance, appearance, etc. shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature ($25\pm10^{\circ}\text{C}$), normal humidity ($45\pm20\%$ RH), and in area not exposed to direct sun light. (Life time of backlight, please refer to Data about backlight.)

Testing Conditions and Inspection Criteria:

For the final test the testing sample must be stored at room temperature for 24 hours, after the tests listed in up Table, Standard specifications for Reliability have been executed in order to ensure stability.

Item	Test Model	In section Criteria
Current Consumption	Refer To Specification	The current consumption should conform to the product specification.
Contrast	Refer To Specification	After the tests have been executed, the contrast must be larger than half of its initial value prior to the tests.
Appearance	Visual inspection	Defect free.

11.SPECIFICATION OF QUALITY ASSURANCE:

11.1 Purpose

This standard for Quality Assurance should affirm the quality of LCD Module products to supply.

11.2 Standard for Quality Test

a. Inspection:

Before delivering, the supplier should take the following tests, and affirm the quality of product.

b. Electro-Optical Characteristics:

According to the individual specification to test the product.

c. Test of Appearance Characteristics:

According to the individual specification to test the product.

d. Test of Reliability Characteristics:

According to the definition of reliability on the specification for testing products.

e. Delivery Test:

Before delivering, the supplier should take the delivery test.

(i) Test method: According to MIL-STD105E.General Inspection Level II take a single time.

(ii) The defects classify of AQL as following:

Major defect: AQL = 0.65

Minor defect: AQL = 2.5

Total defects: AQL = 2.5

11.3. Nonconforming Analysis & Deal With Manners

a. Nonconforming Analysis:

(i) Purchaser should supply the detail data of non- conforming sample and the non- conforming.

(ii) After accepting the detail data from purchaser, the analysis of nonconforming should be finished in two weeks.

(iii) If supplier can not finish analysis on time, must announce purchaser before two weeks.

b. Disposition of nonconforming:

(i) If find any product defect of supplier during assembly time, supplier must change the good product for every defect after recognition.

(ii) Both supplier and customer should analyze the reason and discuss the disposition of nonconforming when the reason of nonconforming is not sure.

11.4. Agreement items

Both sides should discuss together when the following problems happen.

a. There is any problem of standard of quality assurance, and both sides think that it must be modified.

b. There is any argument item which does not record in the standard of quality assurance.

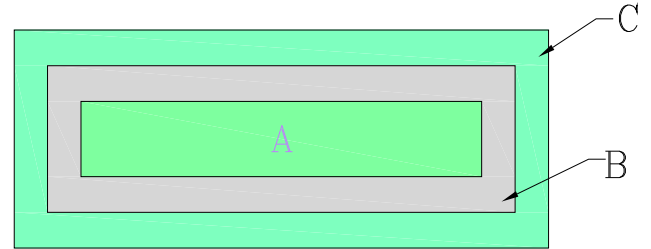
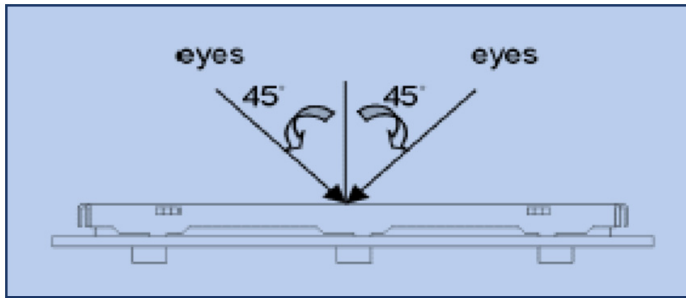
c. Any other special problem.

11.5 Standard of The Product Appearance Test

a. Manner of appearance test: This specification should be applied for both light on and off situation.

(i) The test must be under 20W × 2 or 40W fluorescent light, and the distance of view must be at 30±5cm.

- (ii) When test the model of transmissive product must add the reflective plate.
- (iii) The test direction is base on about around 10° of vertical line (Left graph)
- (iii) Temperature: $25 \pm 5^\circ\text{C}$ Humidity: $65 \pm 10\%\text{RH}$



(iv) Definition of area (Right graph)

A. Area: Viewing area. B. Area: Out of viewing area.(Outside viewing area)

b. Basic principle:

- (i) It will accord to the AQL when the standard can not be described.
- (ii) The sample of the lowest acceptable quality level must be discussed by both supplier and customer when any dispute happened.
- (iii) Must add new item on time when it is necessary.

c. Standard of inspection: (Unit: mm)

Allowable limits defined in follow Dot defect Table should be met for each white, black , R, G, B raster. The limits apply to the entire area. Missing white in 60% or more of typical (one color, R or G or B) pixel aperture is defined as a bright defect, less than 60% is acceptable .Black spot in 60% or more of typical pixel aperture is defined as a dark defect, less than 60% is acceptable.

Dot defect table:

Item		White dot defect	Black dot defect	Total
1	Defect counts	3	3	3
2	Combined defect Counts	No combined dot defect allowed. Two Single dot defect that within 5mm during each dot defect should becounated as combined dot defect.		

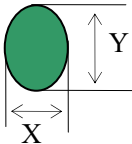
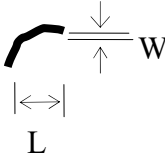
11.6 Inspection specification

AQL inspection standard

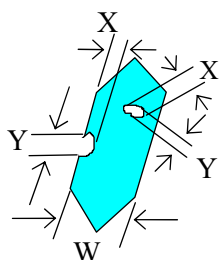
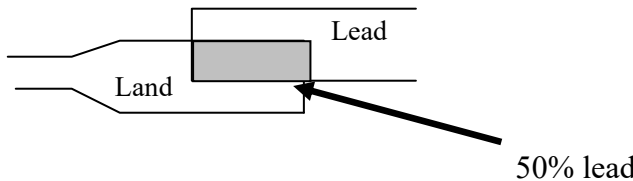
Sampling method: MIL-STD-105E, Level II, single sampling

Classify	Item		Note	AQL	
Major	Display state	Short or open circuit	1	0.65	
		Contrast defect (dim, ghost)			
		LC leakage			
		Flickering			
		No display			
		Wrong viewing direction	2		
		Wrong Back-light	7		
	Non-display	Flat cable or pin reverse	9		
		Wrong or missing component	10		
Minor	Display state	Background color deviation	2	2.5	
		Black spot and dust	3		
		Line defect	4		
		Scratch			
		Rainbow	5		
		Pin hole	6		
	Polarizer	Bubble and foreign material	3		
		Scratch	4		
	PCB,FPC	Scratch	4		
	Soldering	Poor connection	8		
	Wire	Poor connection	9		
	LCD	CHIP OUT	11		

Note on defect classification:

No.	Item	Criterion																				
1	Short or open circuit	Not allow																				
	LC leakage																					
	Flickering																					
	No display																					
	Wrong viewing direction																					
	Wrong Back-light																					
2	Contrast defect	Refer to approval sample																				
	Background color deviation																					
3	Point defect, Black spot, dust (incl. Polarizer) ex.: dirt under polarizer, Pinhole of reflector ,glass scratch, dirt under glass,scratch on polarizer $\phi = (X+Y)/2$	 <table><tr><td>Point Size</td><td>Acceptable Qty.</td></tr><tr><td>$\phi \leq 0.20$</td><td>Disregard</td></tr><tr><td>$0.20 < \phi \leq 0.25$</td><td>3</td></tr><tr><td>$0.25 < \phi \leq 0.30$</td><td>2</td></tr><tr><td>$\phi > 0.30$</td><td>0</td></tr></table> Unit: mm	Point Size	Acceptable Qty.	$\phi \leq 0.20$	Disregard	$0.20 < \phi \leq 0.25$	3	$0.25 < \phi \leq 0.30$	2	$\phi > 0.30$	0										
	Point Size	Acceptable Qty.																				
$\phi \leq 0.20$	Disregard																					
$0.20 < \phi \leq 0.25$	3																					
$0.25 < \phi \leq 0.30$	2																					
$\phi > 0.30$	0																					
4	Line defect	 <table><tr><td></td><td>Line W</td><td>Acceptable Qty.</td></tr><tr><td>L</td><td>W</td><td></td></tr><tr><td>---</td><td>$0.015 \geq W$</td><td>Disregard</td></tr><tr><td>$3.0 \geq L$</td><td>$0.03 \geq W$</td><td rowspan="2">2</td></tr><tr><td>$2.0 \geq L$</td><td>$0.05 \geq W$</td></tr><tr><td>$1.0 \geq L$</td><td>$0.1 > W$</td><td>1</td></tr><tr><td>---</td><td>$0.05 < W$</td><td>Applied as point defect</td></tr></table> Unit: mm		Line W	Acceptable Qty.	L	W		---	$0.015 \geq W$	Disregard	$3.0 \geq L$	$0.03 \geq W$	2	$2.0 \geq L$	$0.05 \geq W$	$1.0 \geq L$	$0.1 > W$	1	---	$0.05 < W$	Applied as point defect
			Line W	Acceptable Qty.																		
L	W																					
---	$0.015 \geq W$	Disregard																				
$3.0 \geq L$	$0.03 \geq W$	2																				
$2.0 \geq L$	$0.05 \geq W$																					
$1.0 \geq L$	$0.1 > W$	1																				
---	$0.05 < W$	Applied as point defect																				
5	Rainbow	Not more than two color changes across the viewing area																				

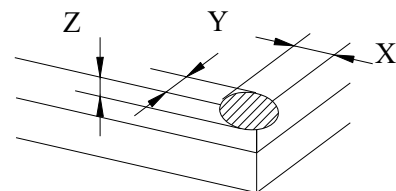
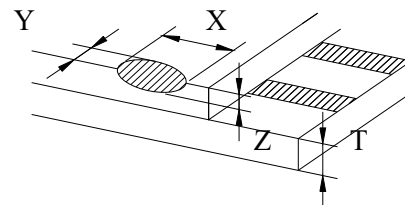
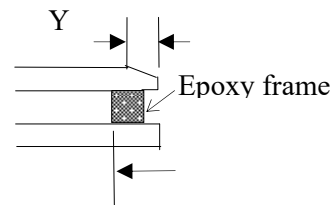
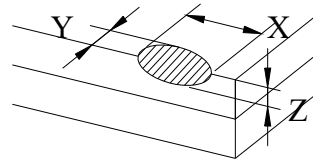
No.	Item	Criterion
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6	Segment pattern W = Segment width $\phi = (X+Y)/2$	(1) Pin hole $\phi < 0.10\text{mm}$ is acceptable.  <table border="1" data-bbox="968 358 1428 560"> <tr> <th>Point Size</th> <th>Acceptable Qty</th> </tr> <tr> <td>$\phi \leq 1/4W$</td> <td>Disregard</td> </tr> <tr> <td>$1/4W < \phi \leq 1/2W$</td> <td>1</td> </tr> <tr> <td>$\phi > 1/2W$</td> <td>0</td> </tr> </table> Unit: mm	Point Size	Acceptable Qty	$\phi \leq 1/4W$	Disregard	$1/4W < \phi \leq 1/2W$	1	$\phi > 1/2W$	0
Point Size	Acceptable Qty									
$\phi \leq 1/4W$	Disregard									
$1/4W < \phi \leq 1/2W$	1									
$\phi > 1/2W$	0									
7	Back-light	(1) The color of backlight should correspond its specification. (2) Not allow flickering								
8	Soldering	(1) Not allow heavy dirty and solder ball on PCB or FPC. (The size of dirty refer to point and dust defect) (2) Over 50% of lead should be soldered on Land. 								
9	Wire	(1) Copper wire should not be rusted (2) Not allow crack on copper wire connection. (3) Not allow reversing the position of the flat cable. (4) Not allow exposed copper wire inside the flat cable.								
10	PCB,FPC	(1) Not allow screw rust or damage. (2) Not allow missing or wrong putting of component.								

11

LCD

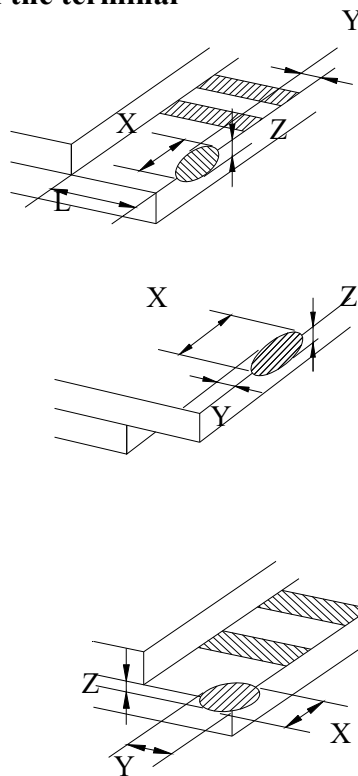
2.1.1 chip on the surface



Note: A: LCD Length

X	Y	Z
$>1/8A$	$\leq 0.3\text{mm}$	$\leq 1/2T$
$\leq 1/8A$	Not enter into epoxy frame	$\leq T$
	Not enter into the inner edge of epoxy	$\leq 1/2T$

2.1.2 Chip on the terminal

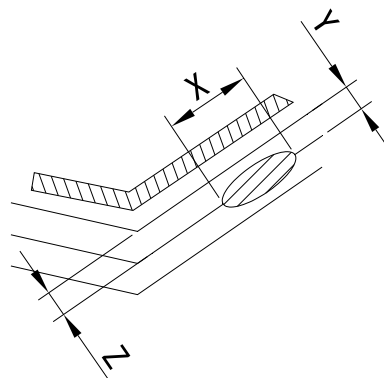


X	Y	Z
$>1/8A$	$\leq 0.3\text{mm}$	$\leq 1/2T$
$\leq 1/8A$	$\leq 1/2L$	$\leq T$
$\leq 1/8A \& \leq 1\text{mm}$	$\leq L$	$\leq T$
$\leq 1/8A \& \leq 2\text{mm}$	$\leq L$	$\leq 1/2T$

Note: A: LCD Length.

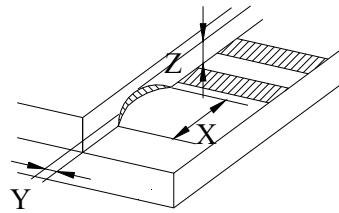
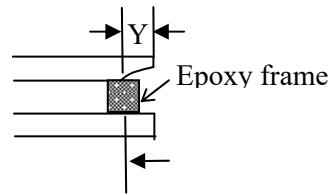
the distance between crack and contact pad must be greater than the width of 1st contact pad.

2.1.3 Chip out on between side



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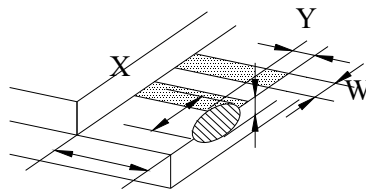
LCD



X	Y	Z
$\leq 1/8A$	Not enter into epoxy frame	$Z \leq 2T$
	Not enter into 1/2 epoxy frame	$Z \leq 1/2T$

Note: A : LCD Length

2.1.4 including corner chip and side chip

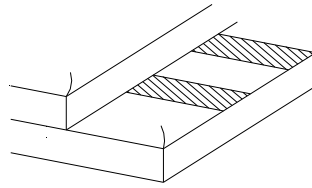


L

Note: A:LCD Length

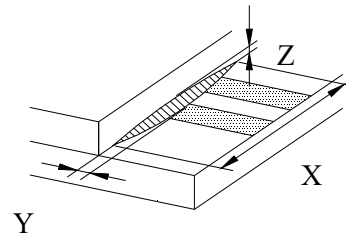
X	Y	Z
$>1/8A$	$\leq 1/6L$	$\leq 1/2T$
$\leq 1/8A$	$\leq 1/3L$	
$\leq 1/4W$	$\leq 2/3L$	

2.2 Chip out



- 1) Chip out is that crackles extend to inner edge.
- 2) Crackles round epoxy frame will be rejected.
- 3) Chip out on the terminal will be rejected: $Z=T$ length $>1\text{mm}$ or $Z<T$ length $>2\text{mm}$
- 4) The chip out at ITO will be rejected.

2.3 Poor cutting



X	Y	Z
$>1/8$ A	≤ 0.3	$\leq 1/2T$
$\leq 1/8$ A	According to drawing	$1/2T \leq Z \leq T$

Note : A: LCD Length.

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LCD

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SMT

According to the <Acceptable of electronic assemblies>
IPC-A-610C class 2 stander. Component missing or function defect
are Major defect ,the others are Minor defect.

Any one out of the specification will be rejected.

12. GENERAL PRECAUTIONS

(1) Mounting Method

The panel of the LCD Module consists of two thin glass plates with polarizers which easily get damaged since the Module is fixed by utilizing fitting holes in the printed circuit board. Extreme care should be taken when handling the LCD Modules.

(2) Caution of LCD handling & cleaning

When cleaning the display surface, use soft cloth with solvent (recommended below) and wipe lightly.

- Isopropyl alcohol
- Ethyl alcohol
- Trichlorotrifluoroethane

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface. Do not use the following solvent:

- Water
- Ketone
- Aromatics

(3) Caution against static charge

The LCD Module use C-MOS LSI drivers, so we recommend that you connect any unused input terminal to VDD or VSS, do not input any signals before power is turned on. And ground your body, Work/assembly table. And assembly equipment to protect against static electricity.

(4) Packaging

Modules use LCD elements, and must be treated as such. Avoid intense shock and falls from a height.

- To prevent modules from degradation. Do not operate or store them exposed directly to sunshine or high temperature/humidity.

(5) Caution for operation

- It is indispensable to drive LCD's within the specified voltage limit since the higher voltage than the limit shorten LCD life. An electrochemical reaction due to direct current causes LCD deterioration, Avoid the use of direct current drive.
- Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them.

However those phenomena do not mean malfunction or out of order with LCD's which will come back in the specified operating temperature range.

- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- As light dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal

open circuit.

Usage under the relative condition of 40°C, 50%RH or less is required.

(6) Storage

In the case of storing for a long period of time (for instance, for years) for the purpose or replacement use, The following ways are recommended.

- Storage in a polyethylene bag with sealed so as not to enter fresh air outside in it, And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light is.

Keeping temperature in the specified storage temperature range.

- Storing with no touch on polarizer surface by the anything else. (It is recommended to store them as they have been contained in the inner container at the time of delivery)

(7) Safety

- It is recommendable to crash damaged or unnecessary LCD into pieces and wash off liquid crystal by using solvents such as acetone and ethanol which should be burned up later.
- When any liquid crystal leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

Hello Lighting co., ltd reserves the right to change this specification.

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- END -